# Statement of Volatility − UNCLASSSIFIED.

**Component Information:**

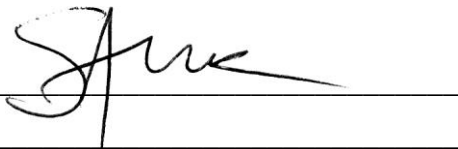| | Vendor Response |
|---|---|
| System Manufacturer: | The Quantum World Corporation |
| Part Description: | Quantum True Random Number Gen. |
| Part Number (As Marked on Equipment): | CS128M |
| Technical Point of Contact: | Scott Wilber |

## Memory Size, Type, Purpose, Input Method, Protection Method.

| Memory Device | Data Response |
|---|---|
| Size: | 3,464,192 bits |
| Type of Memory: | FPGA On Chip RAM |
| Can Programs write data into the device during normal operation? | NO |
| Can the Operating System write data into the device during normal operation? | NO |
| Does the device retain data when powered off? | NO |
| How is data input into the device? | On chip RAM used during operation. |
| How is the device write protected? | No access. |
| Remarks: Internal RAM is not accessible outside the device. | |

| Memory Device | Data Response |
|---|---|
| Size: | 64 Mbit |
| Type of Memory: | Flash. FPGA configuration memory. |
| Can Programs write data into the device during normal operation? | NO |
| Can the Operating System write data into the device during normal operation? | NO |
| Does the device retain data when powered off? | YES |
| How is data input into the device? | Programmer connected to board. Only programmable during manufacture. |
| How is the device write protected? | Tamper-protection bit enabled. |
| Remarks: Writing to configuration memory is disabled when tamper-protection bit is enabled/fused. This process is irreversible. Configuration memory is AES-256 encrypted. | |

| Memory Device | Data Response |
|---|---|
| Size: | 2048 bits |
| Type of Memory: | EEPROM. Product Serial # and product descriptors for USB Interface chip. |
| Can Programs write data into the device during normal operation? | NO |
| Can the Operating System write data into the device during normal operation? | NO |
| Does the device retain data when powered off? | YES |
| How is data input into the device? | Setup program in computer during manufacture. |
| How is the device write protected? | Write-Protect pin physically set to disable writing after manufacture. |
| Remarks: Unintentional writing is disabled by default. Intentional writing requires physical access inside the device enclosure. Device will not work if memory is changed. | |

Signature: _____

Title:  President_____

Date:  10 DECEMBER 2019 _____